

FHEW: Homomorphic Encryption Bootstrapping in less than a Second¹

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Outline

Introduction/Summary

The new NAND gate

Simpler Refreshing

Conclusion

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The evolution of FHE

Fully Homomorphic Encryption has seen drastic changes since Gentry's first proposal:

- ▶ [Rivest,Adleman,Dertouzos'78]: Open problem
- ▶ [Gentry'09]: ideal lattices, sparse subset-sum, squashing, etc.
- ▶ [Gentry,Halevi'11],[Brakerski,Vaikuntanathan'11]: no squash
- ▶ [Brakerski,Vaikuntanathan'11]: Subexponential LWE
- ▶ [Brakerski'12],[Alperin-Sheriff,Peiert'14]: (Polynomial) LWE
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Still, all schemes have a common ingredient:

Key technique

Gentry's FHE bootstrapping

FHE Bootstrapping

All known FHE schemes are based on **noisy** encryption schemes:

- ▶ Decryption is possible only when noise is sufficiently small.
- ▶ Noise grows when computing on ciphertexts.
- ▶ After a while, no more operations can be performed.

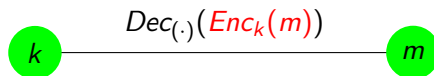
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FHE Bootstrapping:

- ▶ Method to “reset” the noise level of a ciphertext
- ▶ Idea: homomorphically compute ciphertext decryption function on encrypted key



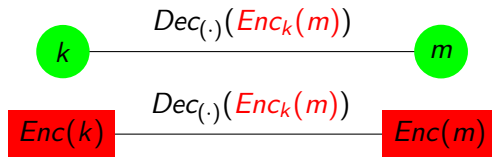
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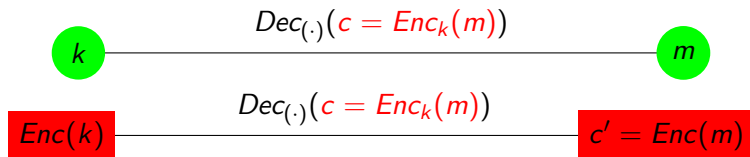
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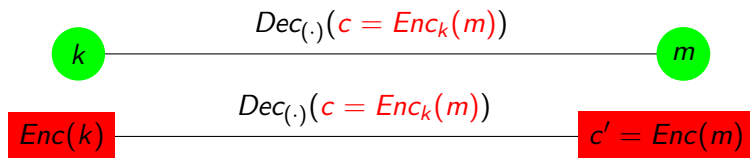
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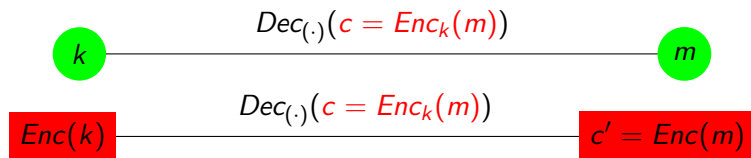
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The quality/noise of the output c' depends on

- ▶ the quality/noise of $Enc(k)$, which is a fresh ciphertext, and
- ▶ the complexity of $Dec_{(.)}(c)$,
- ▶ but not the quality/noise of c , as long as it decrypts

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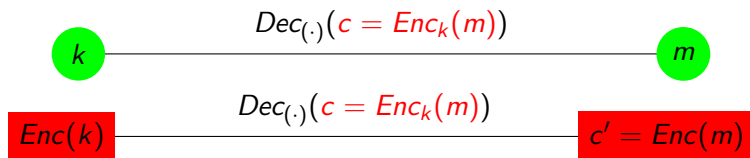
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- ▶ Basic homomorphic properties
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Lattice Cryptography

- ▶ Basic homomorphic properties
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Still, even if $Dec_{(.)}(c)$ is efficient, bootstrapping is very costly because $Dec_{(.)}(c)$ needs to be computed homomorphically on an encrypted $Enc(k)$.

The Problem

FHE Bootstrapping/Refreshing is an expensive process:

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Mitigating the cost of bootstrapping (previous approaches):

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- ▶ Noise control: allow more computation before refreshing
- ▶ HElib: Cost can be amortized over ≈ 1000 binary ciphertext.

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We give a proof of concept solution in **0.6 seconds**:
amortized cost comparable to [HElib], but without the delay...

Two new techniques:

- ▶ a new, **cheap NAND** gate
- ▶ a **simpler refreshing** procedure using ring structure

The new NAND gate

Base: Start from LWE encryption with message space: \mathbb{Z}_t , $t \geq 2$.

Idea: Different message space for input ($t = 4$) and output ($t = 2$).

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Advantages:

- ▶ Cost of computing Homomorphic NAND is negligible (similar to a single private key cryptographic operation.)
- ▶ Excellent noise growth: ϵ grows only by a small constant factor.
- ▶ Substantially simplifies the task faced by the Refreshing procedure.

A simpler refreshing procedure

Base: General approach of [Alperin-Sheriff,Peikert'14] + Ring variant of [Gentry,Sahai,Waters'13] Homomorphic encryption.

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Improvement over [AP14]:

- ▶ Theoretical speed-up of $\tilde{\Omega}(\log^3 q)$
- ▶ Smaller final error.

Combined with the problem simplification brought by our cheap NAND computation, this results in bootstrapping cost ≈ 0.6 second, at estimated ≈ 100 -bit security level.

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LWE and Symmetric Encryption

Definition (Learning with Errors)

- ▶ **For** a random secret $\mathbf{s} \in \mathbb{Z}_q^n$
- ▶ **Given** many sample $(\mathbf{a}, b = \langle \mathbf{a}, \mathbf{s} \rangle + e)$ where $e \leftarrow \chi$, small
- ▶ **Distinguish** the samples from uniformly random

LWE is as hard as worst-case lattice problems

$$\text{Enc}_s(m \in \mathbb{Z}_2) = (\mathbf{a}, b = \langle \mathbf{a}, \mathbf{s} \rangle + e + m \cdot q/2)$$

$$\text{Dec}_s(\mathbf{a}, b) = \lfloor 2(b - \langle \mathbf{a}, \mathbf{s} \rangle) / q \rfloor$$

Sets of encryptions of m with error $e < E$ noted $\text{LWE}_s(m, E)$.

Correct decryption ensured if $(\mathbf{a}, b) \in \text{LWE}_s(\cdot, q/4)$

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Homomorphic Operation on LWE ciphertext

Addition/XOR operation as the sum of ciphertexts:

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(Traditional) (N)AND operation as the tensor of ciphertexts:

$$\text{LWE}_{s_1}(m_1, e_1) \times \text{LWE}_{s_2}(m_2, e_2) \rightarrow \text{LWE}_{s_1 \otimes s_2}(m_1 \wedge m_2, e_1 \cdot e_2)$$

\Rightarrow FHE bootstrapping requires **strong Refreshing** :

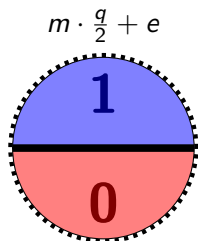
$$\text{LWE}_s(m, e) \rightarrow \text{LWE}_s(m, e'), \quad e' \ll e.$$

Techniques: Key Switching, Mod Switching, and Homomorphic Decryption.

LWE encryption with different message spaces

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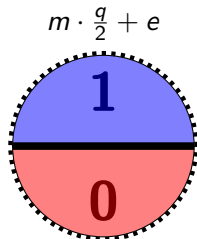


binary messages
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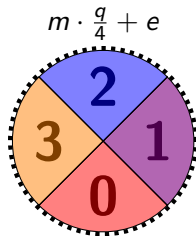
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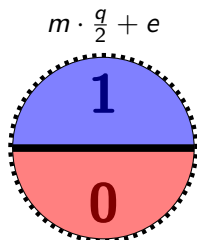


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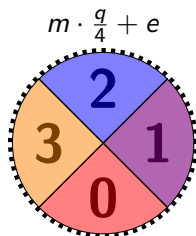
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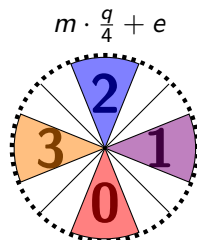
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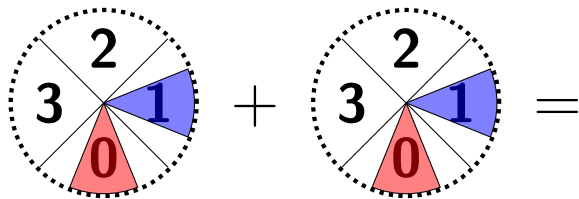


Smaller error
 $\text{LWE}_s^4(m, q/16)$

A Cheap NAND gate

Idea, use: $m_1 \wedge m_2 \Leftrightarrow m_1 + m_2 = 2 \pmod 4$.

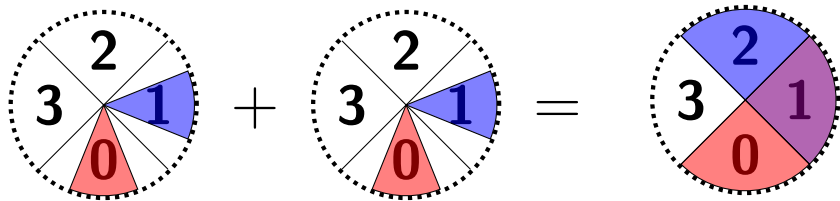
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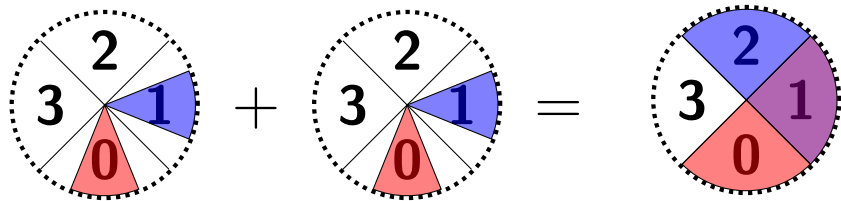
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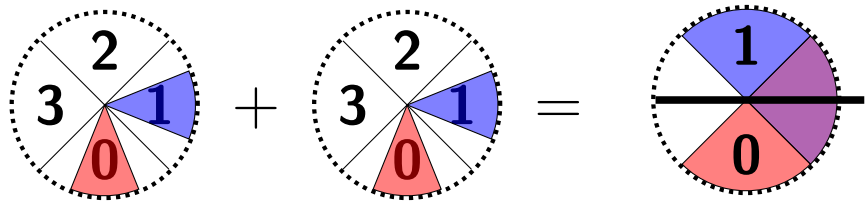


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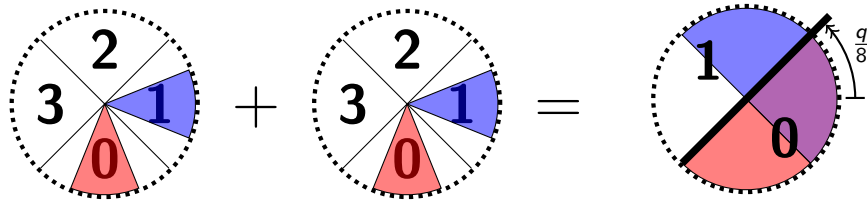


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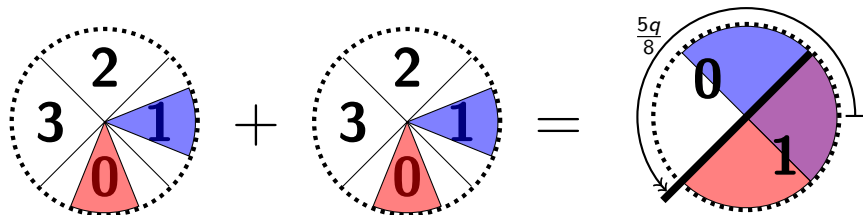


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HomNAND:

$$\text{LWE}_s^4(m_1, \frac{q}{16}) \times \text{LWE}_s^4(m_2, \frac{q}{16}) \rightarrow \text{LWE}_s^2(m_1 \bar{\wedge} m_2, \frac{q}{4})$$
$$(\mathbf{a}_1, b_1) \quad , \quad (\mathbf{a}_2, b_2) \quad \mapsto \quad (\mathbf{a}_1 + \mathbf{a}_2, b_1 + b_2 + \frac{5q}{8})$$

Lightweight Refreshing

We have **HomNAND**:

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To build an FHE we require a relaxed function **LightRefresh**:

$$\mathbf{LightRefresh} : \text{LWE}_s^2 (m, q/4) \rightarrow \text{LWE}_s^4 (m, q/16)$$

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whereas previous works required:

$$\mathbf{Refresh} : \text{LWE}_s^2 (m, q/4) \rightarrow \text{LWE}_s^2 (m, E), E \ll q.$$

As usual, we will use Key Switching, Mod Switching, and Homomorphic Decryption.

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Decryption using an Accumulator

$$\text{Dec}_s(\mathbf{a}, b) = \text{msb}(b - \langle \mathbf{a}, \mathbf{s} \rangle \bmod q) = \text{msb}\left(b - \sum_i a_i \cdot s_i \bmod q\right)$$

$\text{Dec}_s(\mathbf{a}, b)$:

$acc \leftarrow b$

for $i = 1$ to n :

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Return $\text{msb}(acc)$

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Homomorphic decryption given $E'(\mathbf{s}) = [E(a \cdot s_i) \mid i, a]$:

$E(\mathit{acc}) \leftarrow b$

for $i = 1$ to n :

$E(\mathit{acc}) \leftarrow E(\mathit{acc}) - E(a_i \cdot s_i) \bmod q$

Return $\text{LWE}(\text{msb}(\mathit{acc}))$

$\text{ACC} = E(\mathit{acc})$ holds an encrypted integer $\mathit{acc} \in \mathbb{Z}_q$

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- ▶ Initialization: $\text{ACC} \leftarrow b$
- ▶ Addition $\text{ACC} \leftarrow \text{ACC} + c$ of a **fresh ciphertext** $c = E(a \cdot s_i)$
- ▶ Extract encrypted MSB: $\text{ACC} \rightarrow \text{LWE}(\text{msb}(\text{acc}))$

Implementing the Accumulator

The framework of [AP14] based on [GSW13] ($E, +, \cdot$):

- ▶ $ACC = [E(a_{q-1}), \dots, E(a_1), E(a_0)]$ with $a_i = \delta_{i=acc} \in \{0, 1\}$

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We optimize this construction using the cyclotomic ring

$$\mathcal{R} = \mathbb{Z}[X]/(X^N + 1).$$

- ▶ Embed \mathbb{Z}_q in the group $(\{X^i\}_i, \cdot)$, of roots of unity, $q = 2N$.
- ▶ ACC uses only a **single ciphertext** $E(X^{acc})$.

Ring version of [GSW13]

$Q = 2^k$, Gadget matrix: $\mathbf{G} = [\mathbf{I}, 2\mathbf{I}, 4\mathbf{I} \dots 2^{k-1}\mathbf{I}]^t \in \mathbb{Z}_Q^{(n+1) \times (n+1)k}$.

$$E_s(m) = [\mathbf{A}, \mathbf{A}s + \mathbf{e}] + m \cdot \mathbf{G}$$

Dec_s: extract an LWE_s ciphertext (last row) and decrypt.

Supports Add. and Mult. for **small messages** $m \in \{-1, 0, 1\}$.

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Supports Add. and Mult. for **small messages** $m \in \{-1, 0, 1\}$.

Cyclotomic ring $\mathcal{R} = \mathbb{Z}[X]/(X^N + 1)$, $2N = q$ is a power of 2.

Generalized Gadget matrix: $\mathbf{G} = u \cdot [\mathbf{I}, b\mathbf{I}, b^2\mathbf{I} \dots b^{k-1}\mathbf{I}]^t \in \mathcal{R}_Q^{2 \times 2k}$.

$$E_s(m \in \mathbb{Z}_q) = [\mathbf{a}, \mathbf{a} \cdot s + \mathbf{e}] + X^m \cdot \mathbf{G}$$

Supports addition for all message $m \in \mathbb{Z}_q$.

The group of roots of unity and msb

$$\begin{array}{c} m \\ X^m \end{array} \left| \begin{array}{cccc} 0 & 1 & \dots & \frac{q}{2} - 1 \\ 1 & X & \dots & X^{N-1} \end{array} \right| \begin{array}{cccc} \frac{q}{2} & \frac{q}{2} + 1 & \dots & q - 1 \\ -1 & -X & \dots & -X^{N-1} \end{array}$$

Take the vector representation of X^m

$$\mathbf{x}_m \left| \begin{array}{cccc} \begin{bmatrix} 1 \\ 0 \\ \vdots \\ 0 \end{bmatrix} & \begin{bmatrix} 0 \\ 1 \\ \vdots \\ 0 \end{bmatrix} & \dots & \begin{bmatrix} 0 \\ 0 \\ \vdots \\ 1 \end{bmatrix} \\ \begin{bmatrix} -1 \\ 0 \\ \vdots \\ 0 \end{bmatrix} & \begin{bmatrix} 0 \\ -1 \\ \vdots \\ 0 \end{bmatrix} & \dots & \begin{bmatrix} 0 \\ 0 \\ \vdots \\ -1 \end{bmatrix} \end{array}$$

Sum all the coordinates

$$\langle \mathbf{1}, \mathbf{x}_m \rangle \left| \begin{array}{cccc} 1 & 1 & \dots & 1 \\ -1 & -1 & \dots & -1 \end{array} \right.$$

$$\frac{\langle \mathbf{1}, \mathbf{x}_m \rangle + 1}{2} = \frac{(-1)^{\text{msb}(m)} + 1}{2} = \text{msb}(m).$$

Extracting $\text{LWE}_s^4(\text{msb}(m))$

Recall: $\mathbf{G} = u \cdot [\mathbf{I}, b\mathbf{I}, b^2\mathbf{I} \dots b^{k-1}\mathbf{I}]^t \in \mathcal{R}_Q^{2 \times 2k}$ and

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Set $u = q/8$, take the 2^{nd} row, in vector representation:

$$\mathbf{C} = \left[\mathbf{A}', \mathbf{A}' \cdot \mathbf{s} + \mathbf{e} + \frac{Q}{8} \cdot \mathbf{x}_m \right]$$

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Sum all rows and add $q/8$:

$$\begin{aligned} \mathbf{1}^t \cdot \mathbf{C} &= \mathbf{1}^t \cdot \left[\mathbf{A}', \mathbf{A}' \cdot \mathbf{s} + \mathbf{e} + \frac{Q}{8} \cdot \mathbf{x}_m + \frac{Q}{8} \right] \\ &= \left[\mathbf{a}', \mathbf{a}' \cdot \mathbf{s} + e' + \frac{Q}{4} \cdot \text{msb}(m) \right] \end{aligned}$$

Obtain an LWE encryption of $\text{msb}(m)$ with message space \mathbb{Z}_4 .

Improvements

Improvement over the bootstrapping of [AP14]:

- ▶ Generic $\tilde{\Omega}(n)$ speed-up from Ring structure
- ▶ An extra $\tilde{\Omega}(\log^3 q)$ speed-up by **embedding**
- ▶ Error after bootstrapping reduced by $O(\sqrt{n} \log n)$.

In addition to our new NAND gate, implementation becomes reasonable.

Outline

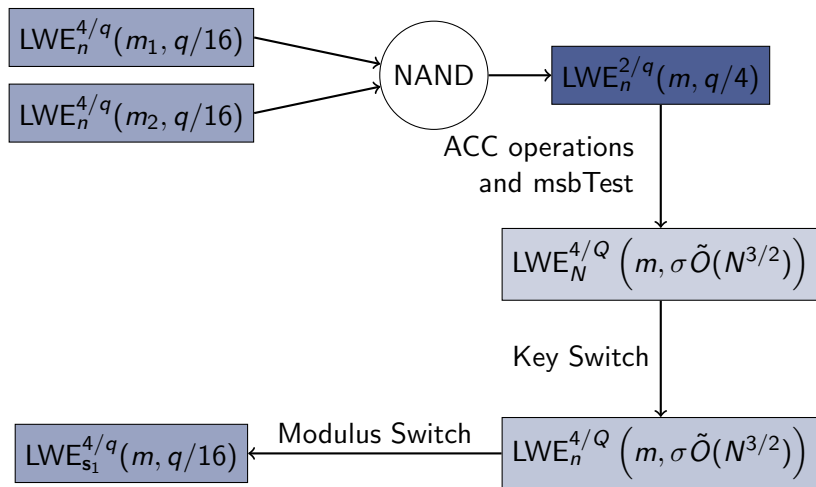
Introduction/Summary

The new NAND gate

Simpler Refreshing

Conclusion

The ciphertext cycle



Parameter Proposal

Parameters.

LWE parameters: $n = 410$ $q = 512.$
Ring-GSW parameters: $N = 1024$ $Q = 2^{32}.$
Gadget Matrix: $Q/8 \cdot [\mathbf{I}, 2^{11} \cdot \mathbf{I}, 2^{22} \cdot \mathbf{I}]$

Key Size.

Bootstrapping Key Size 846 MB
Key Switching Key Size: +135 MB } $\leq 1\text{GB}$

Running time.

Per NAND gate: 39,360 FFTs ≈ 0.4 sec

Security.

Security of the LWE scheme $\delta_1 = 1.0060$
Security of the Ring-GSW scheme $\delta_2 = 1.0060$

Proof of Concept Implementation

- ▶ Coded in 4 days·man room for implementation level optimization.
- ▶ Reasonably concise: ≤ 600 lines of C++ code
[HElib]: $\approx 20,000$ lines
- ▶ Using FFT³ over \mathbb{C} at double precision in dimension 2048 to obtain negacyclic-FFT.
potentially slower than 32-bits NTT in dimension 1024.

Result: Homomorphic NAND & refreshing in 0.61 seconds on a single standard 64-bit core at 3Ghz.

Comparable to the amortized cost of bootstrapping in [HElib].

³FFTW library: The Fastest Fourier Transform in the West